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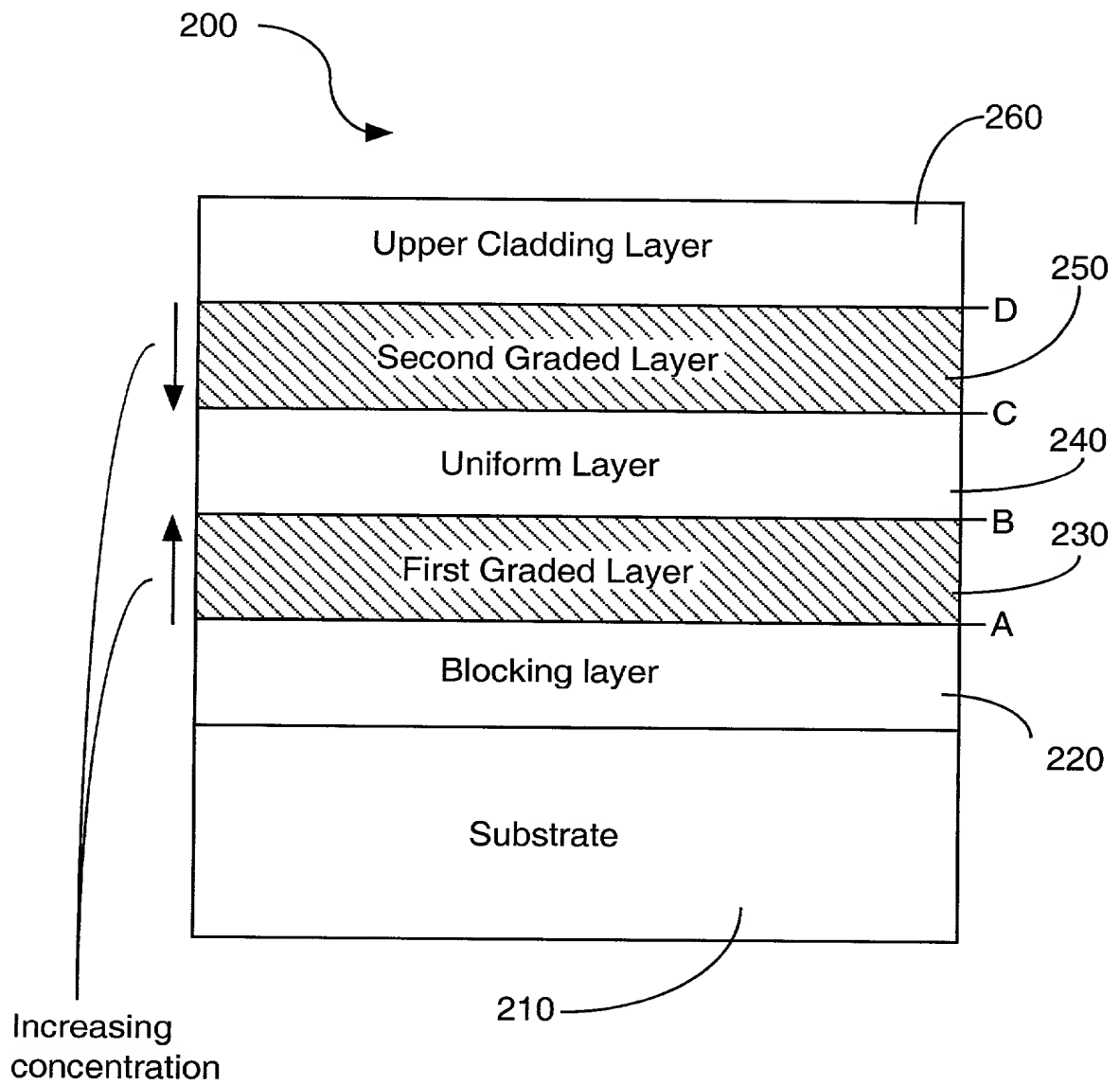


Figure 2

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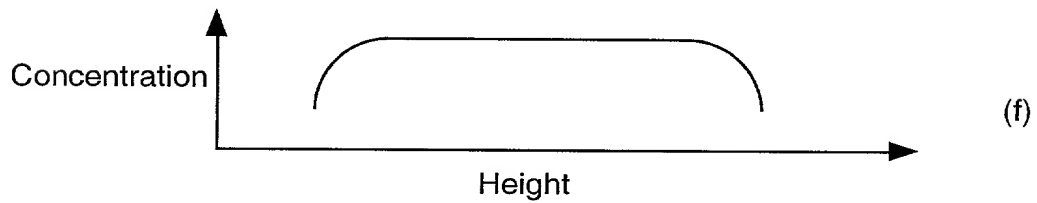
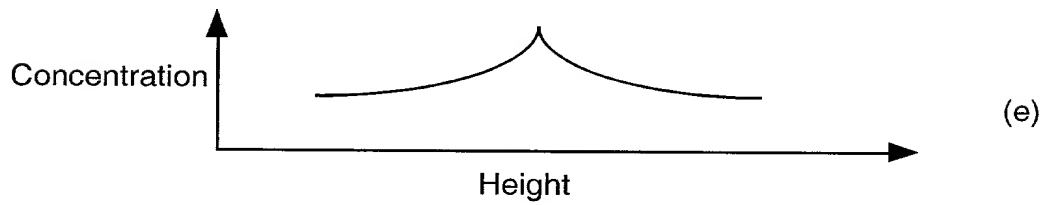
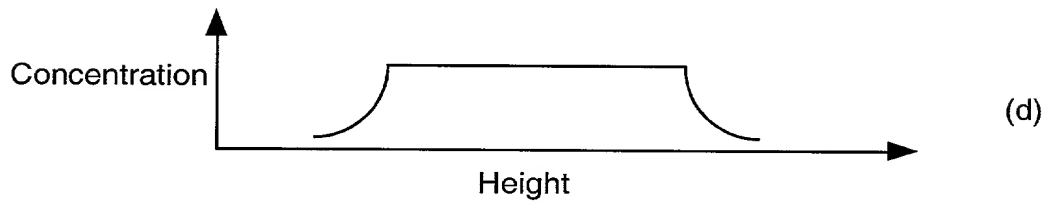
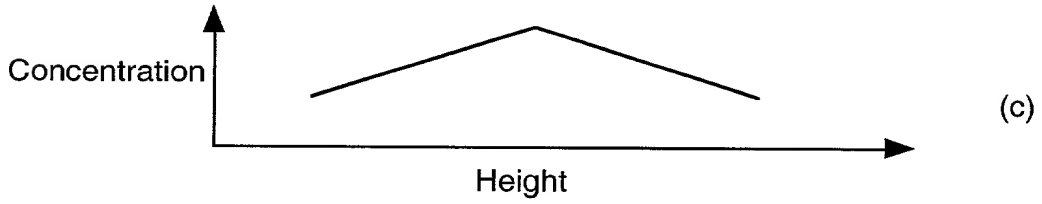
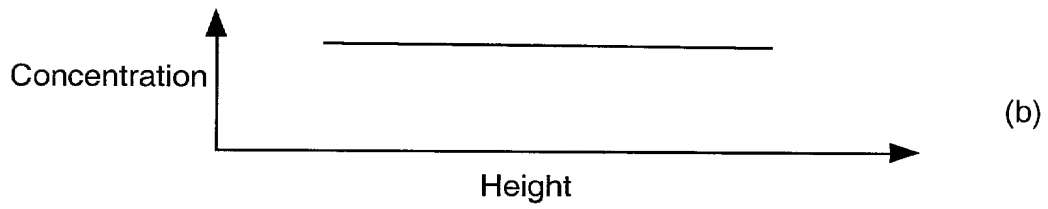
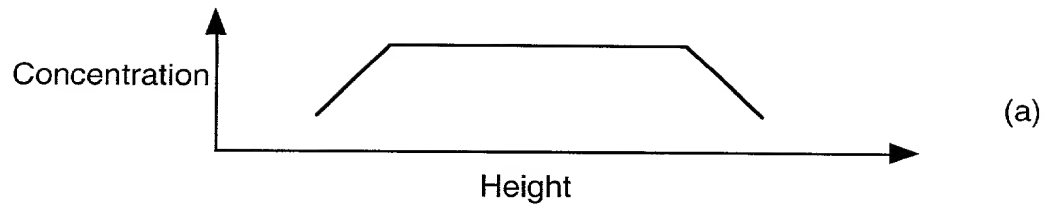


Figure 3

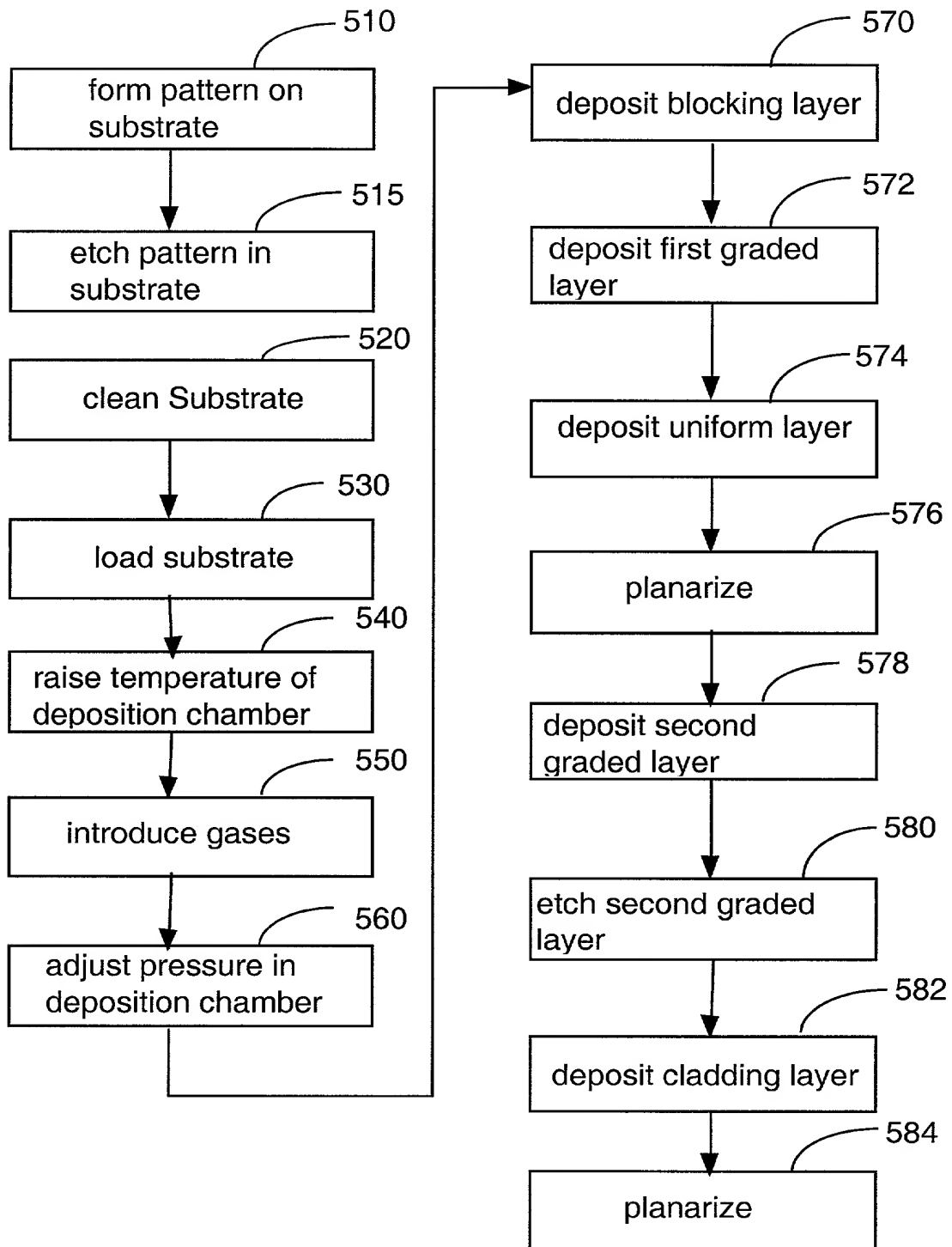


Figure 4

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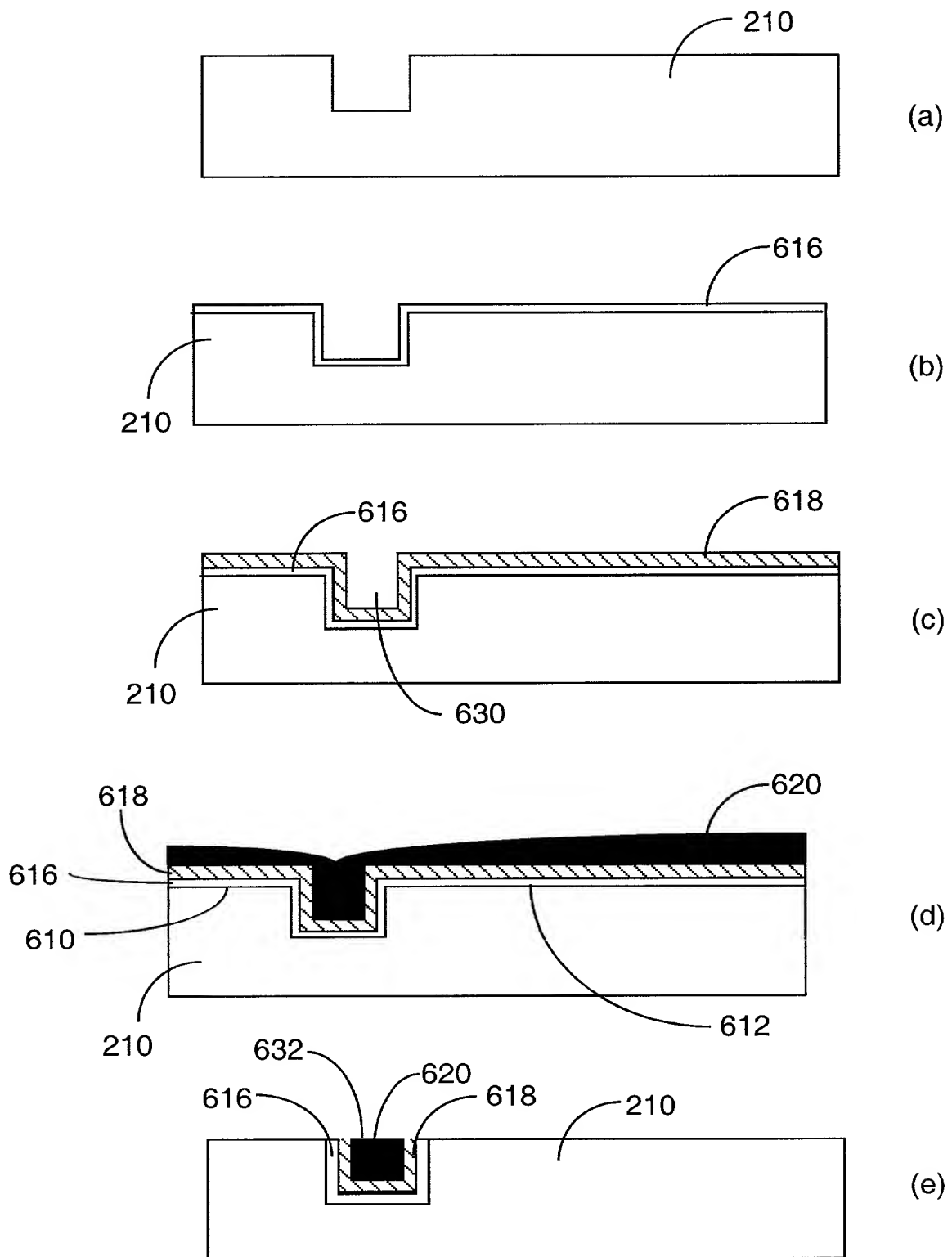


Figure 5

Figure 5 is a schematic diagram illustrating a cross-sectional view of a device structure. The diagram shows a substrate 210 with a central region 616. A layer 620 is formed on the substrate 210, and a layer 618 is formed on the layer 620. A layer 622 is formed on the layer 618. A layer 624 is formed on the layer 622. The layer 624 is formed on the layer 622, and the layer 622 is formed on the layer 618. The layer 618 is formed on the layer 620, and the layer 620 is formed on the substrate 210. The central region 616 is a square region. The layer 620 is a rectangular region. The layer 618 is a rectangular region. The layer 622 is a rectangular region. The layer 624 is a rectangular region.

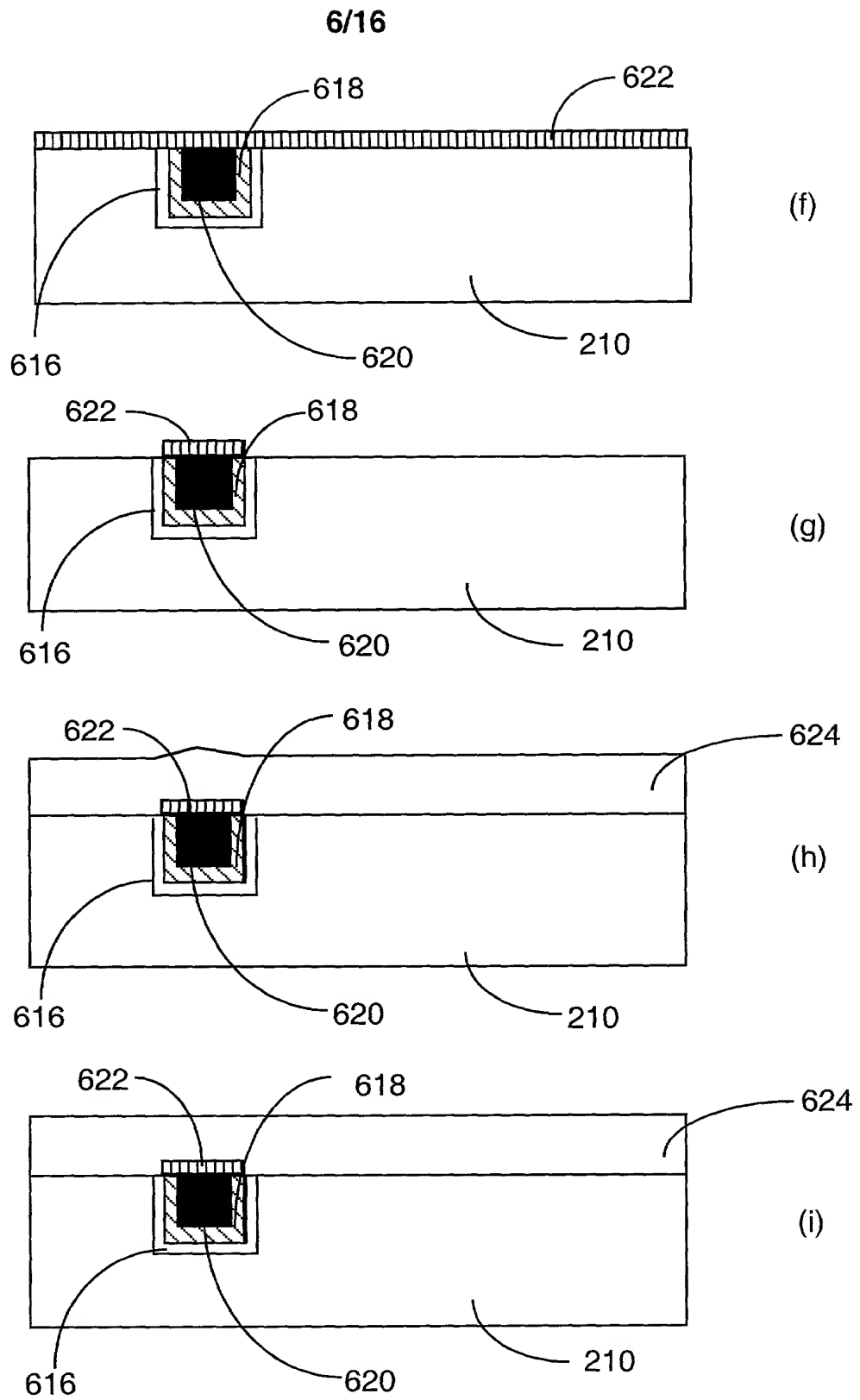
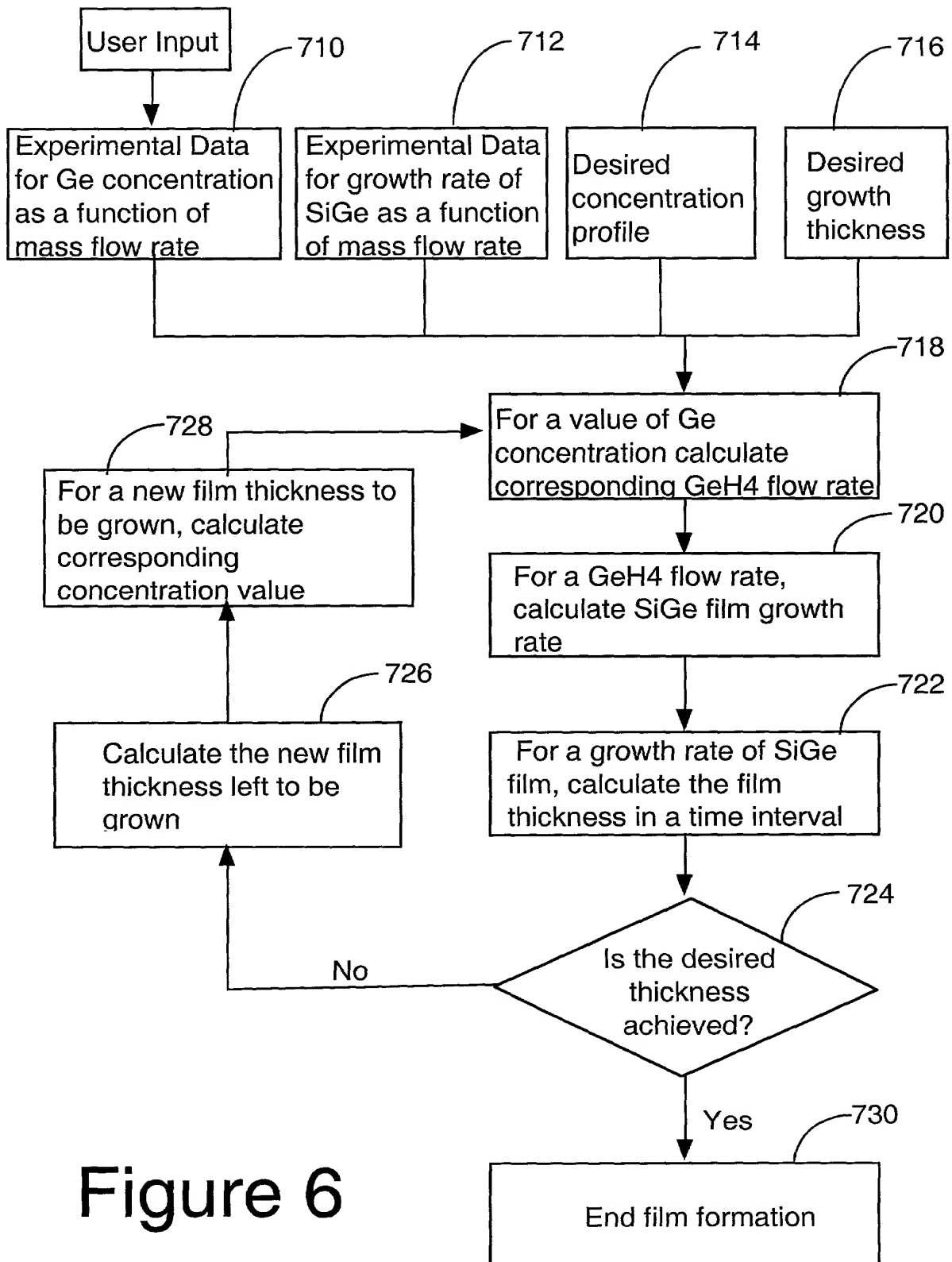


Figure 5

**Figure 6**

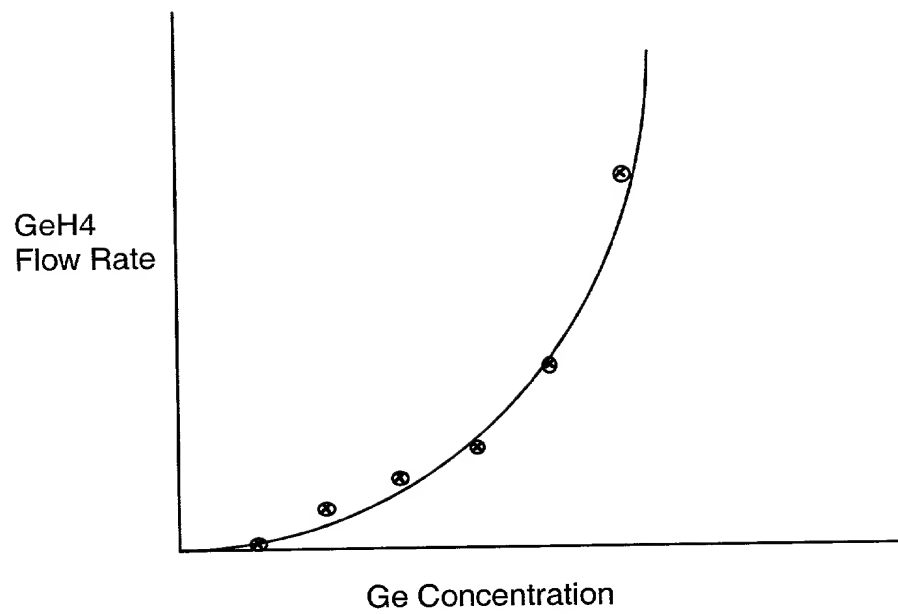


Figure 7

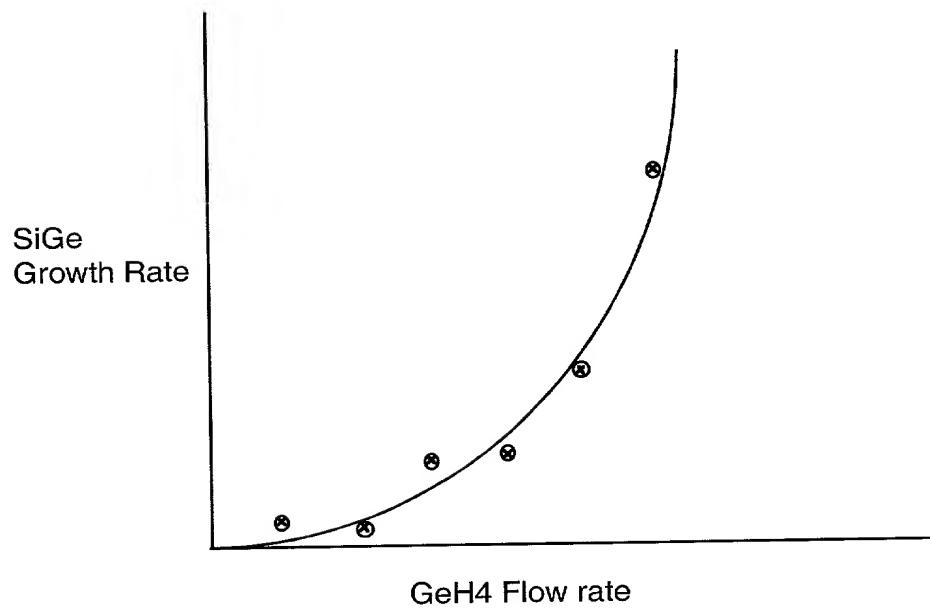


Figure 8

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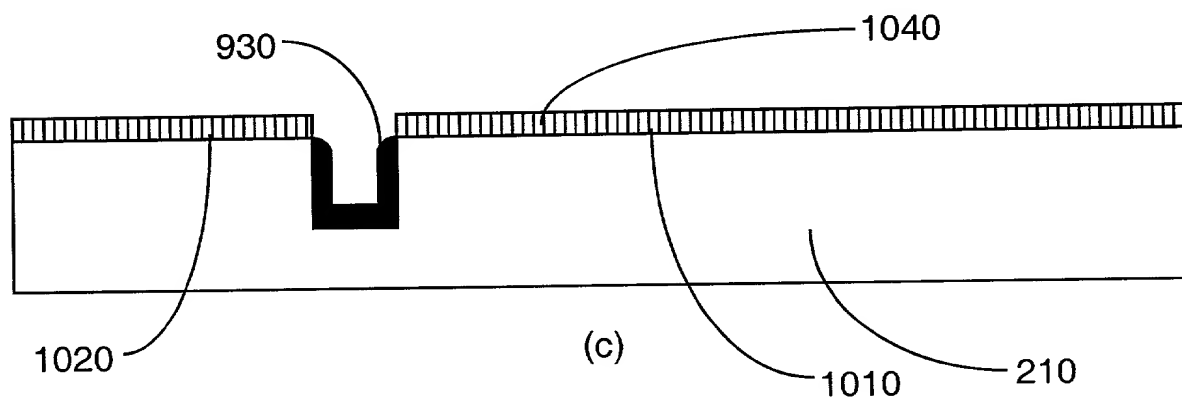
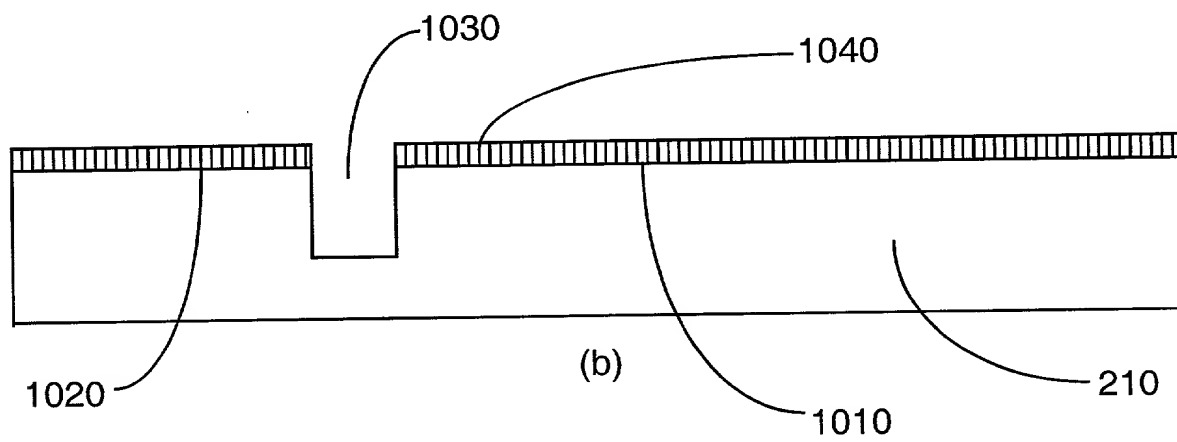
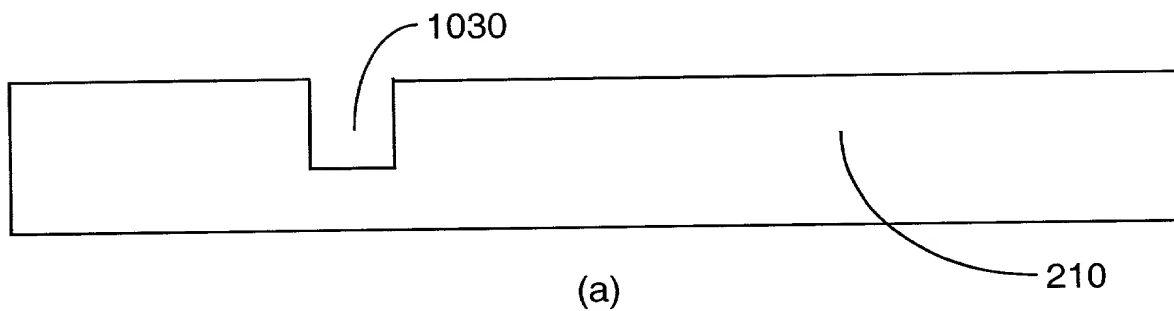


Figure 9

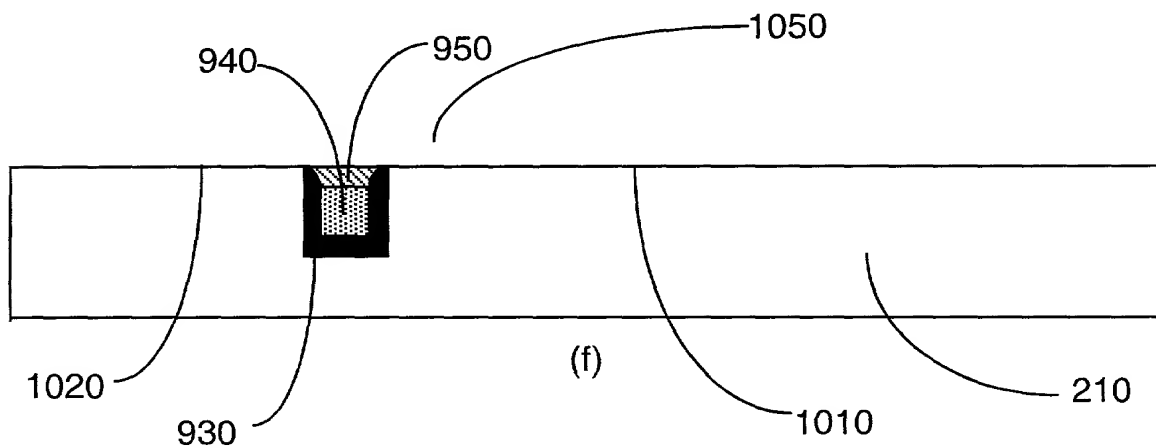
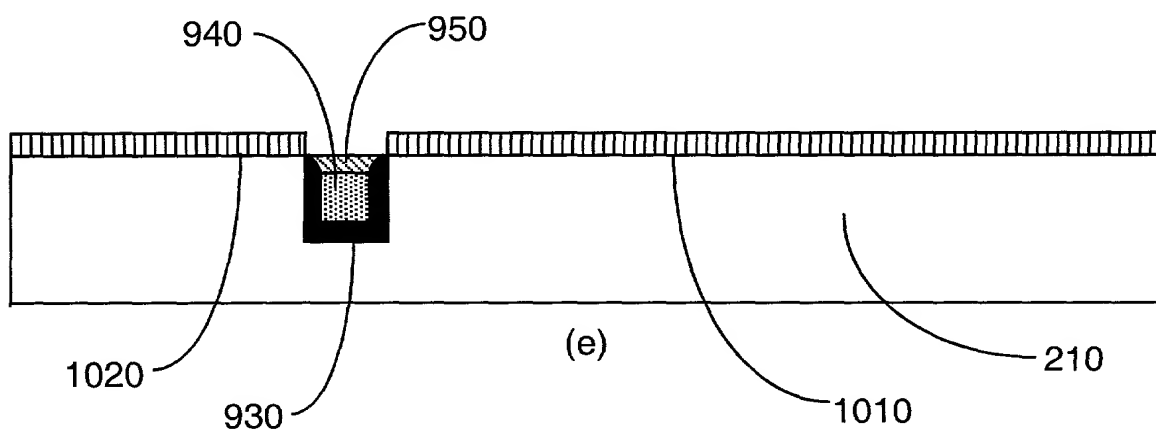
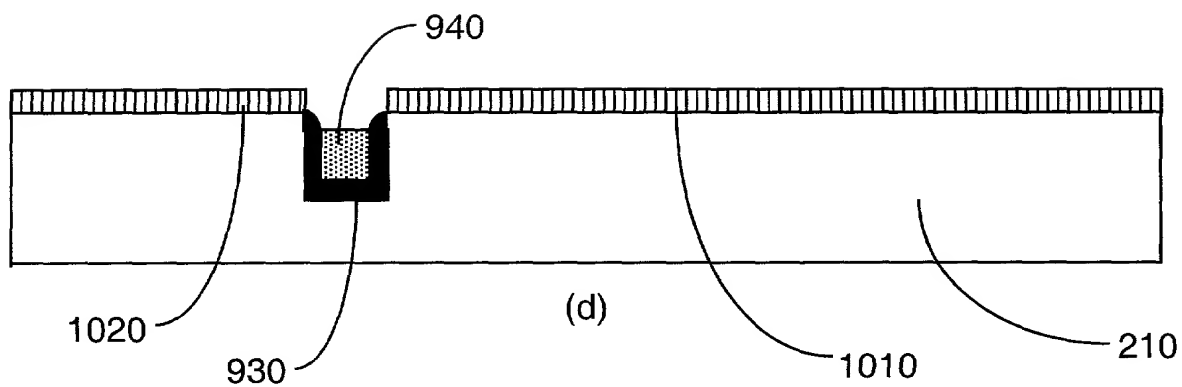


Figure 9

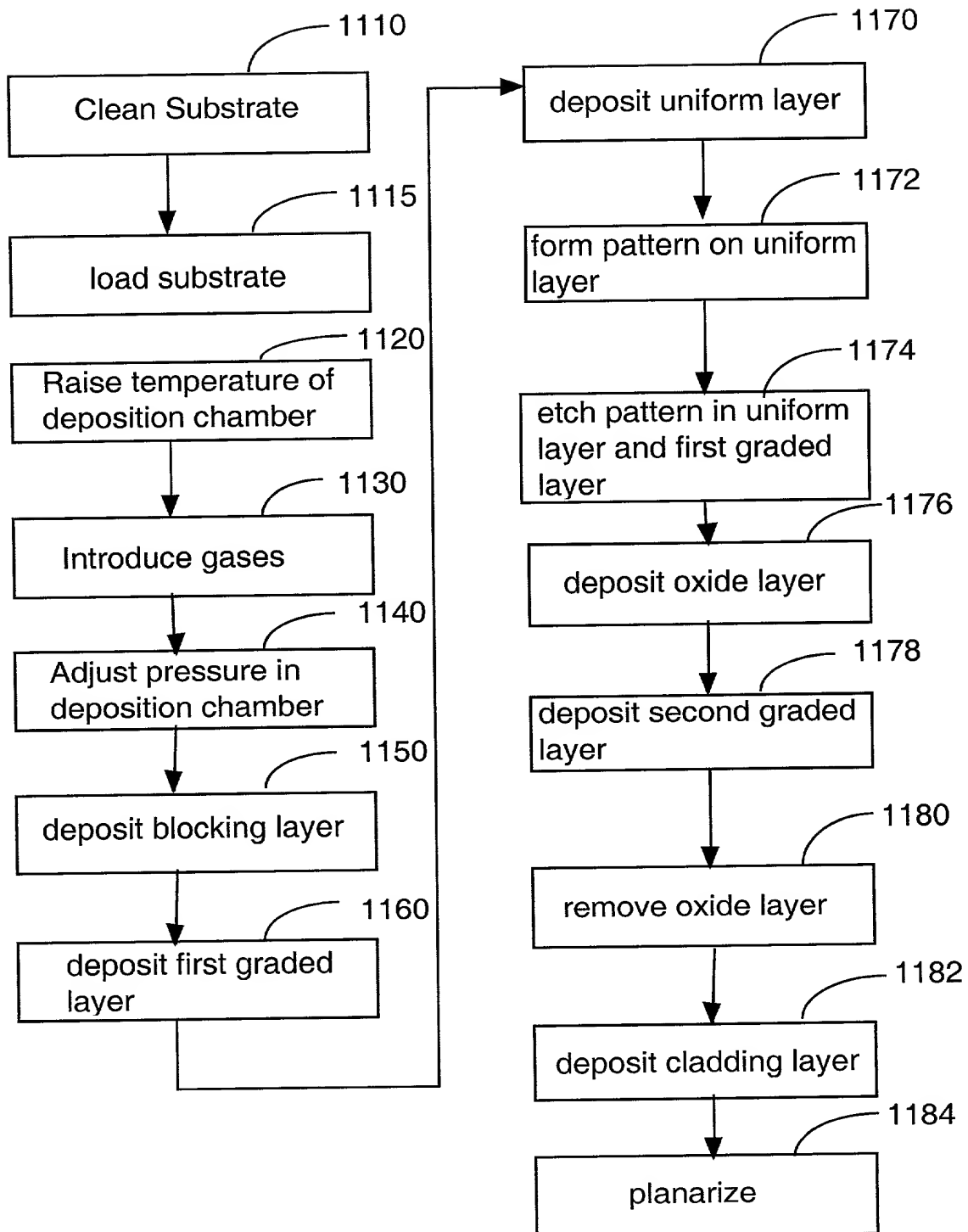


Figure 10

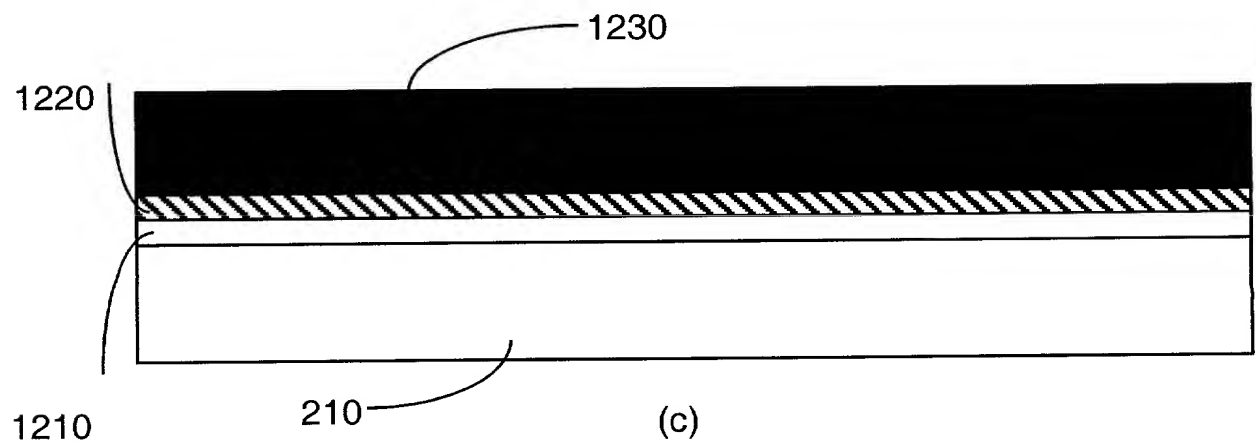
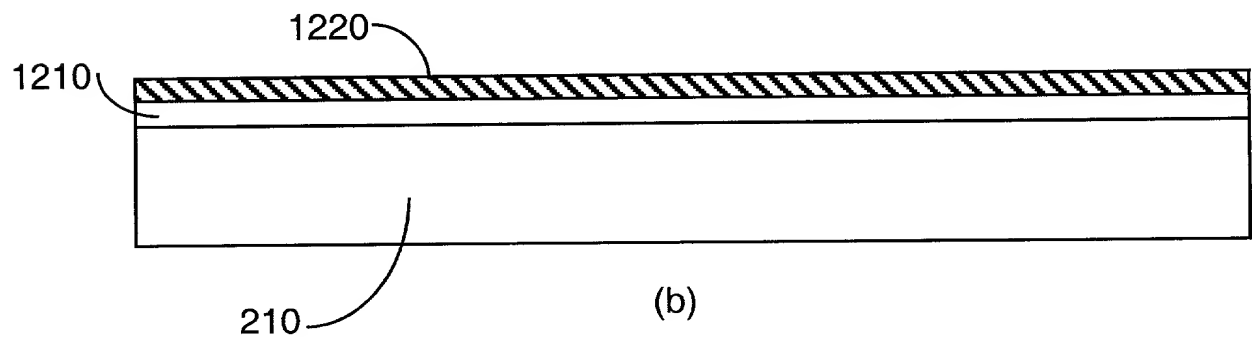
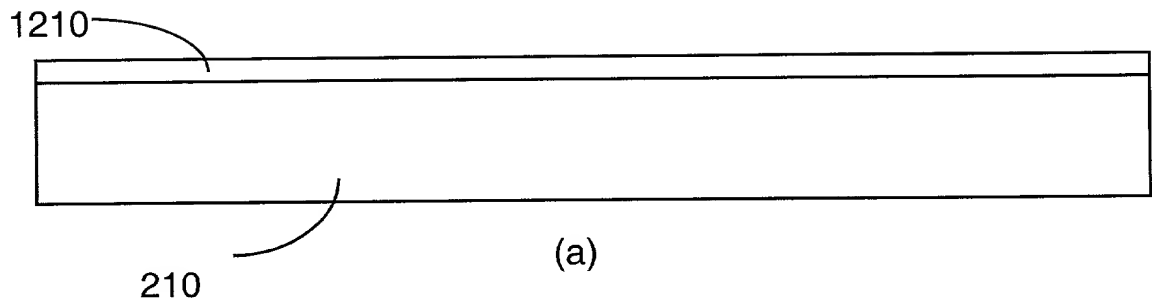


Figure 11

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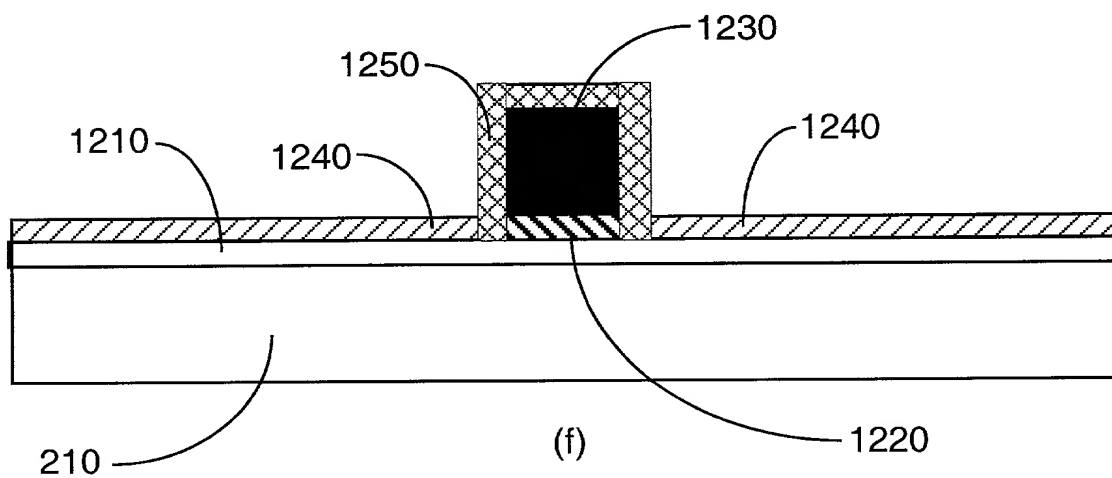
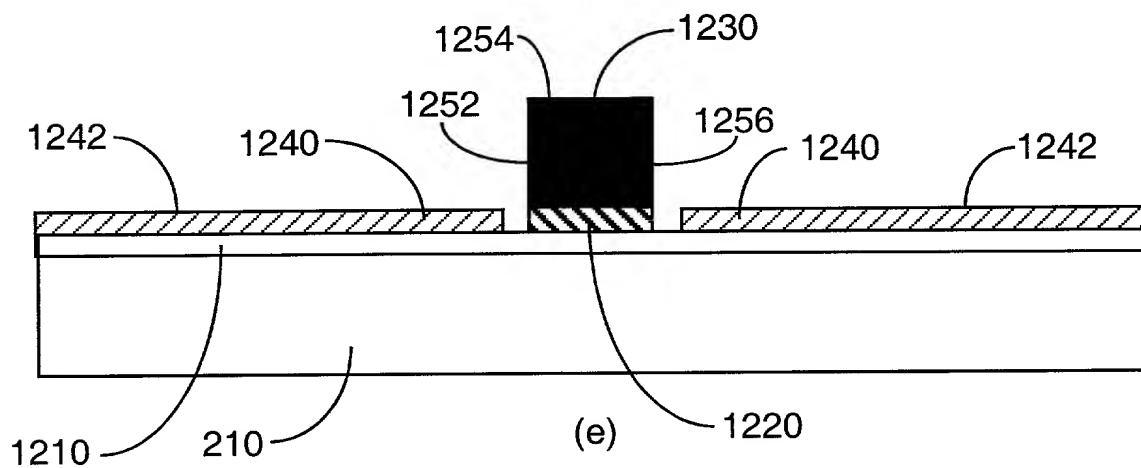
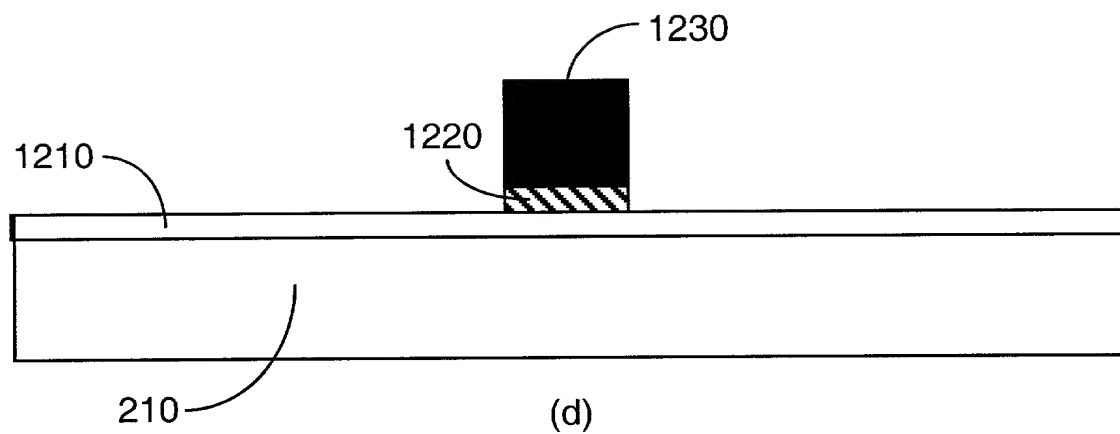


Figure 11

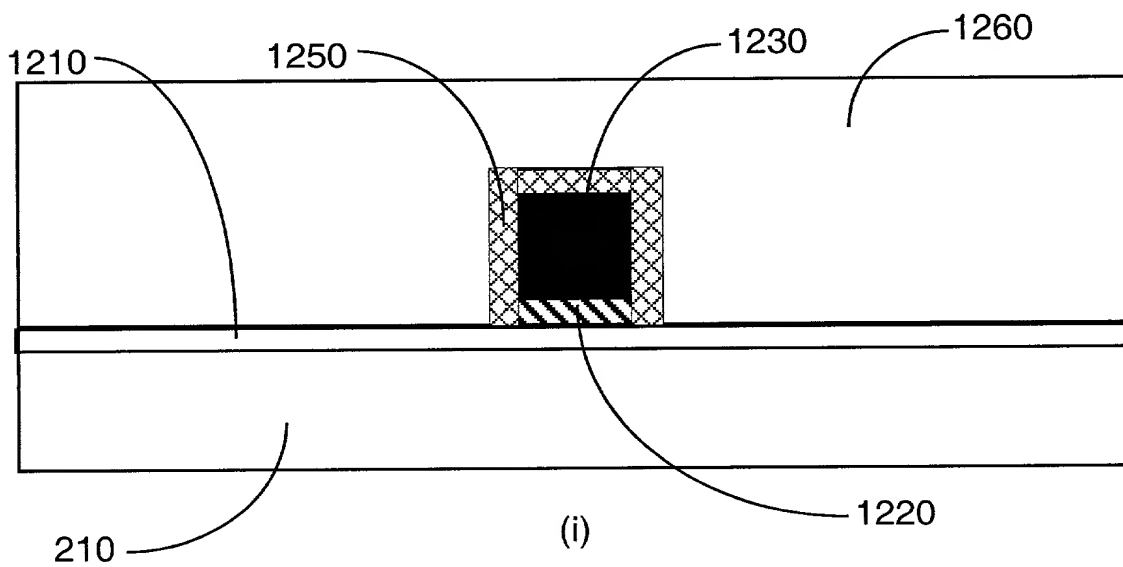
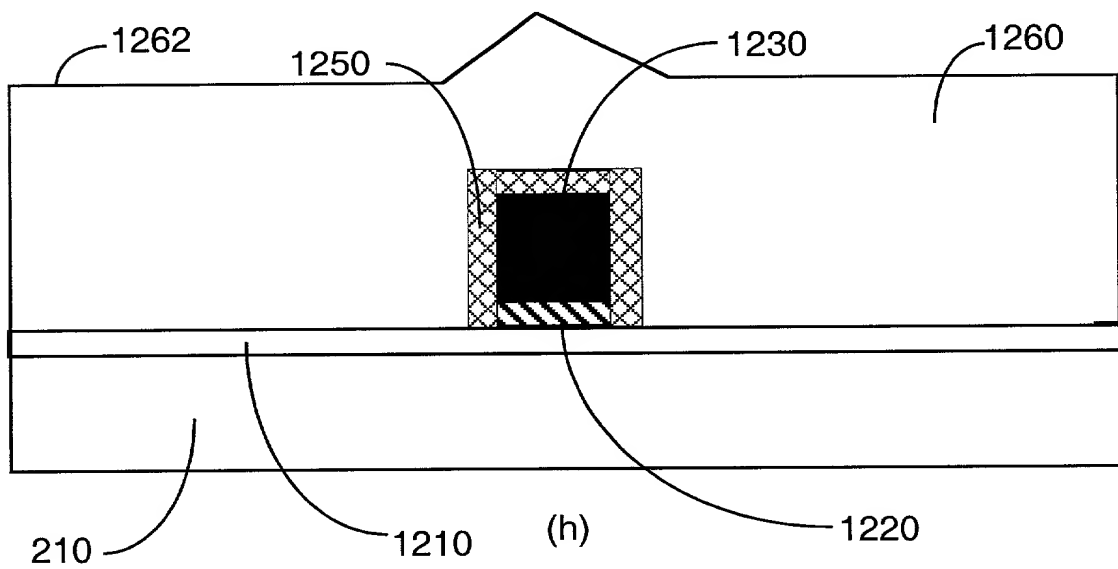
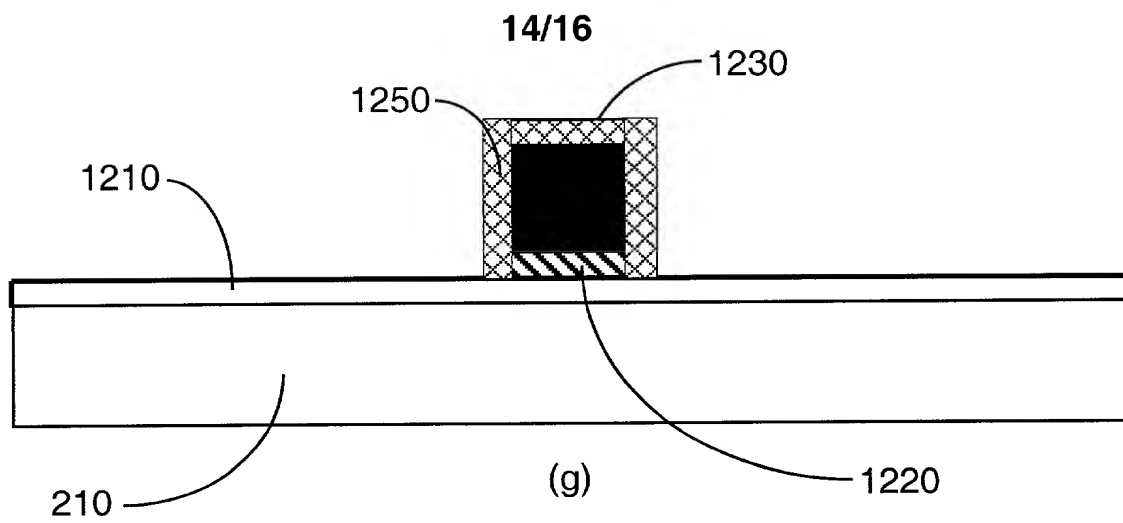


Figure 11

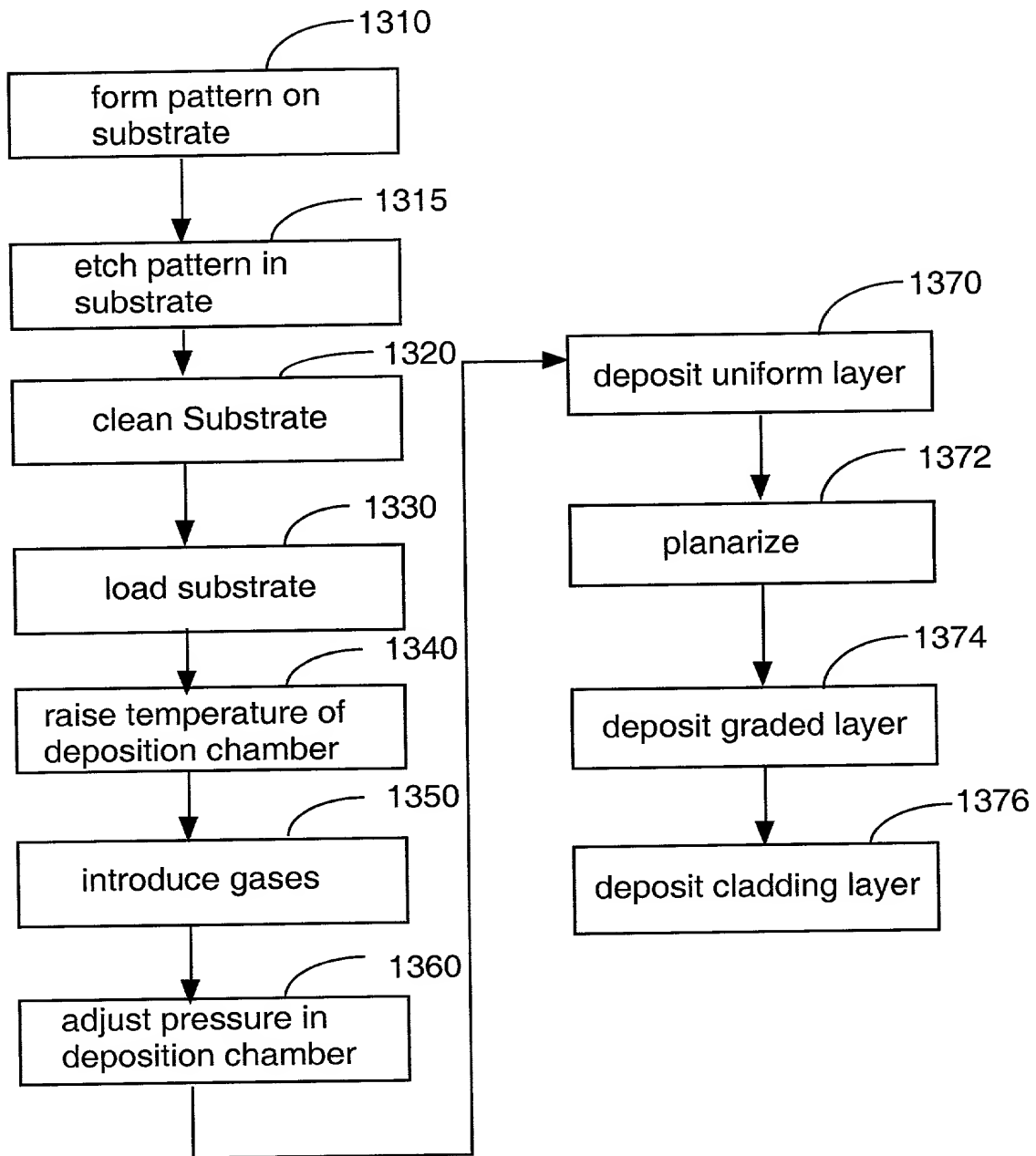


Figure 12

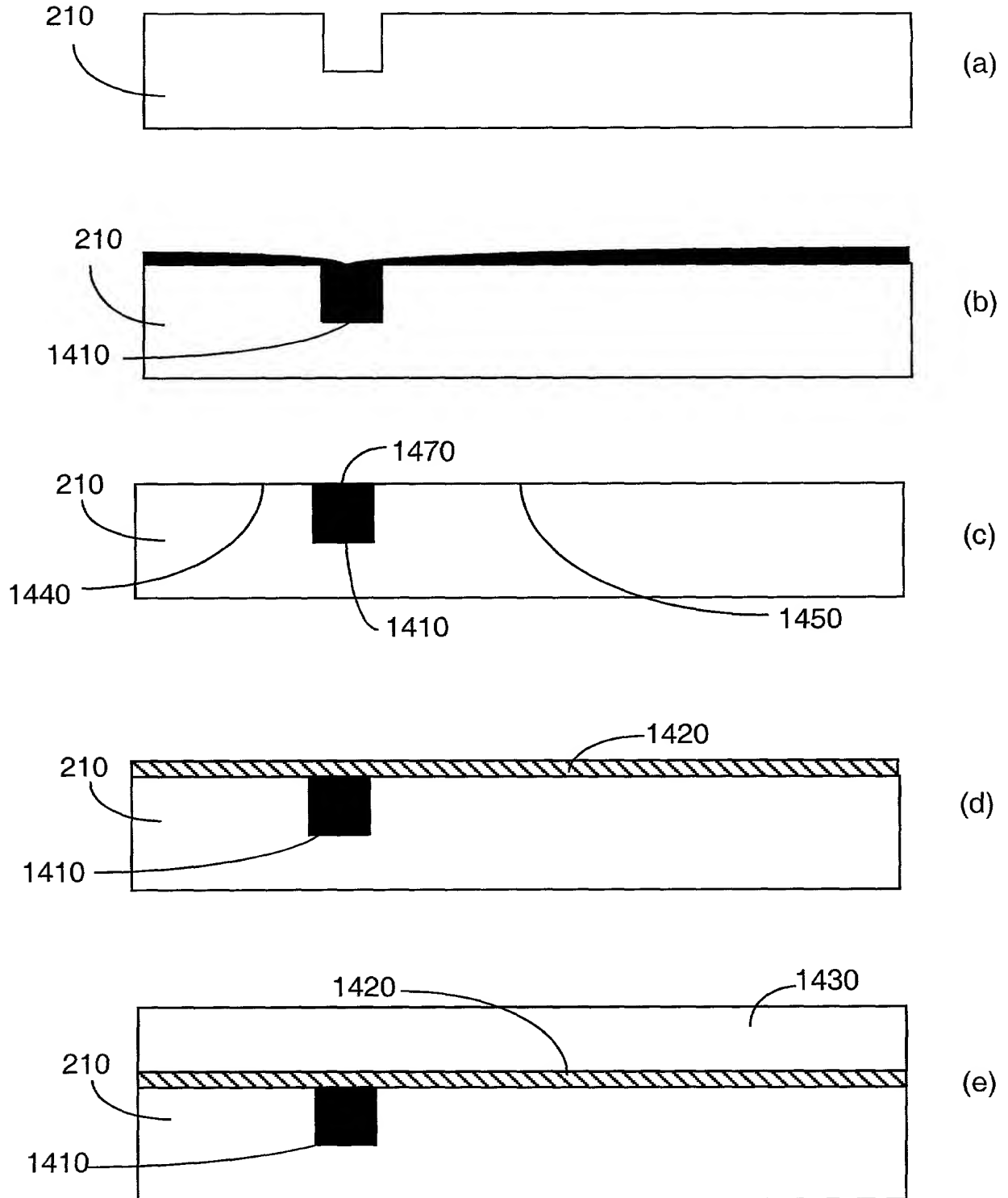


Figure 13